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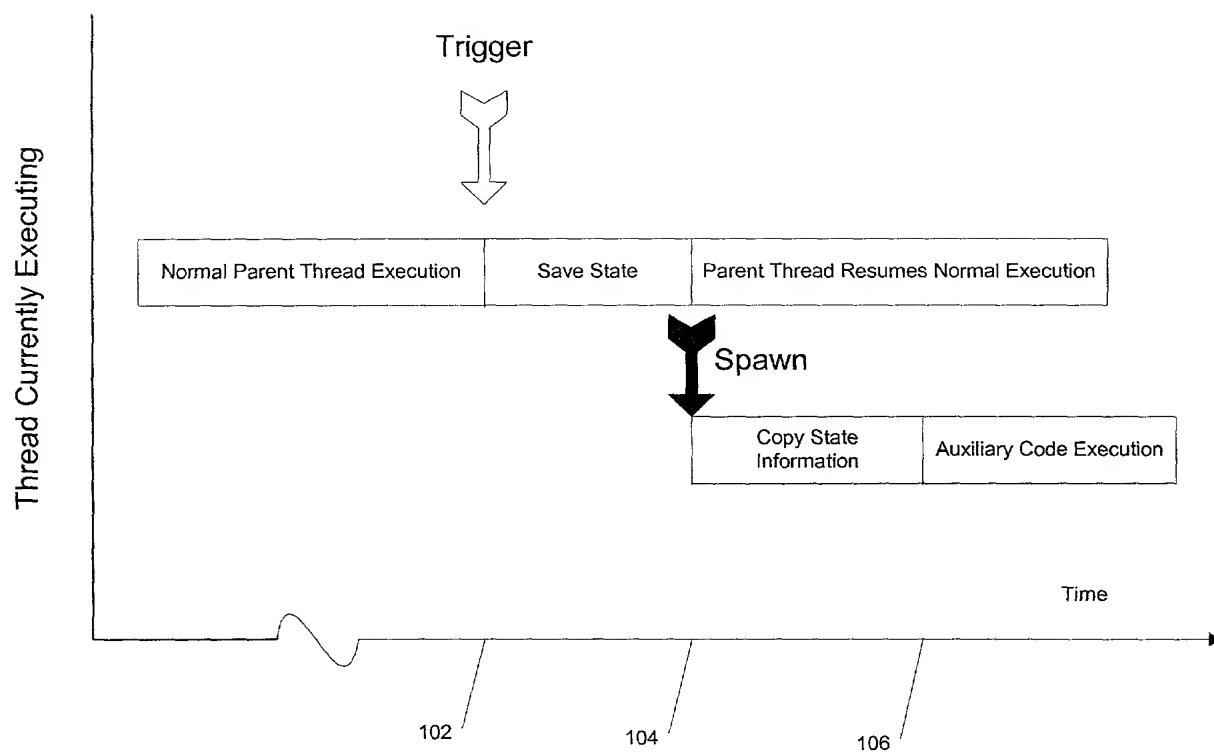


Figure 1

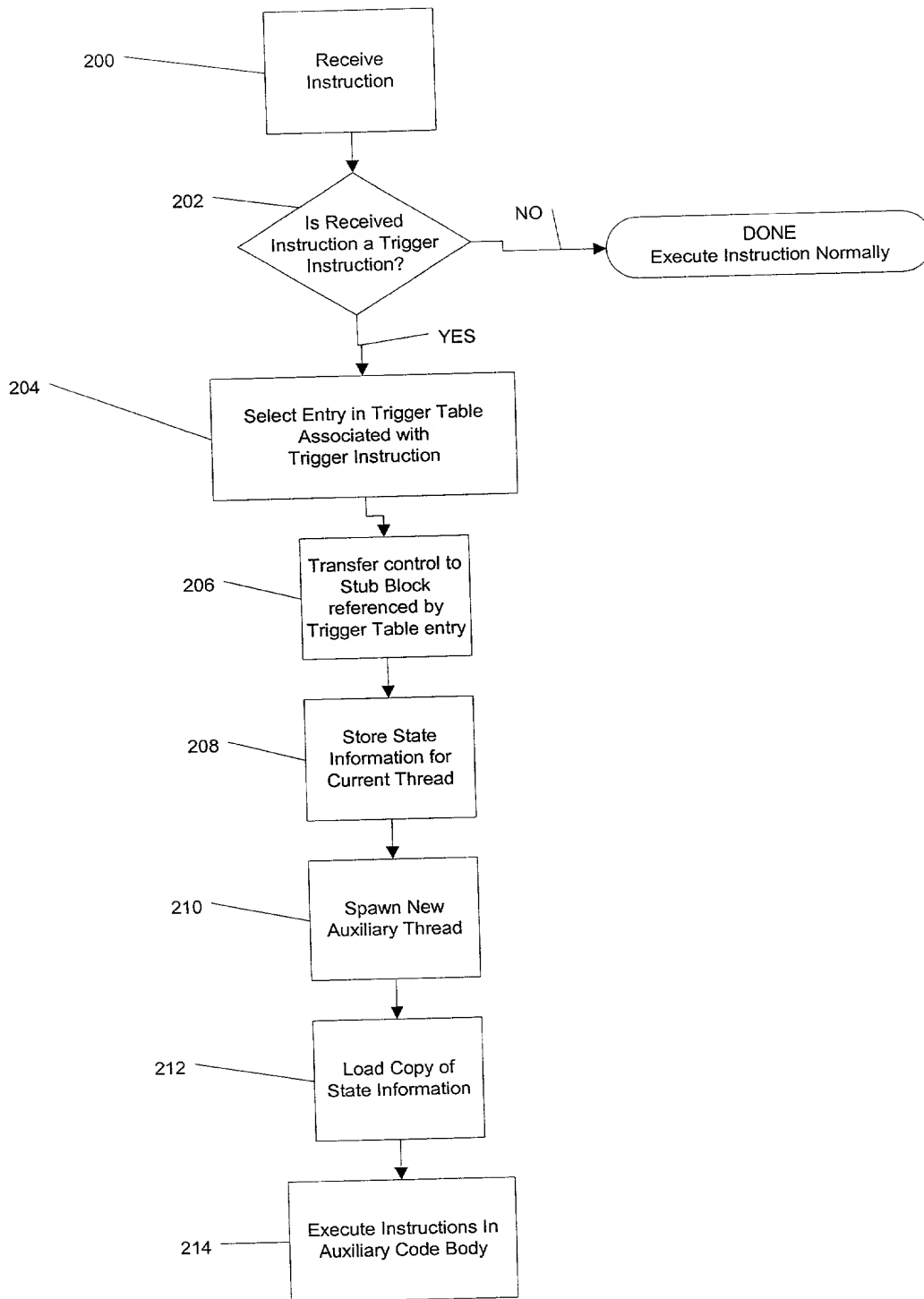


Figure 2

Figure 1 is a schematic diagram of a memory layout. It shows a vertical stack of sections. The top section is labeled "Function Body 306". Below it are three sections labeled "Auxiliary Code 308". Below these is a section labeled "Auxiliary Code (P-slice Code) 309". Below this is a section labeled "Trigger table 310". Below the trigger table is a section labeled "Data Section 304". The entire stack is labeled "Code Section 302" on the left. Vertical ellipses indicate that there are more sections between the auxiliary code and the trigger table, and between the trigger table and the data section.

Figure 3

FIG. 4

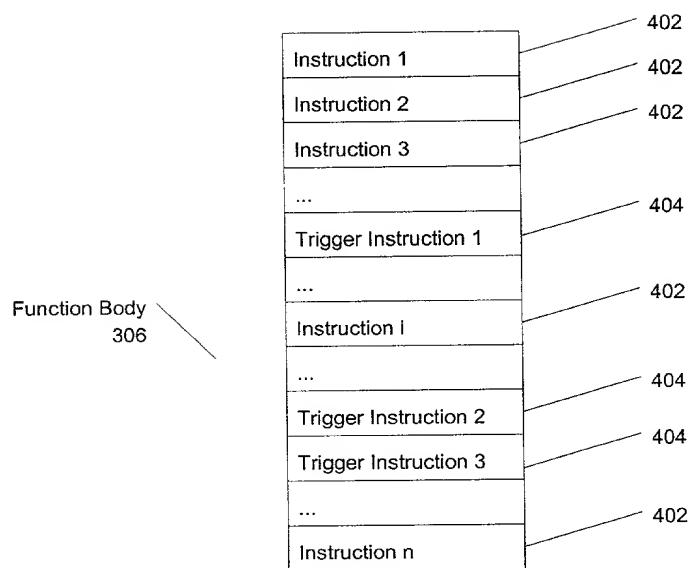


Figure 4

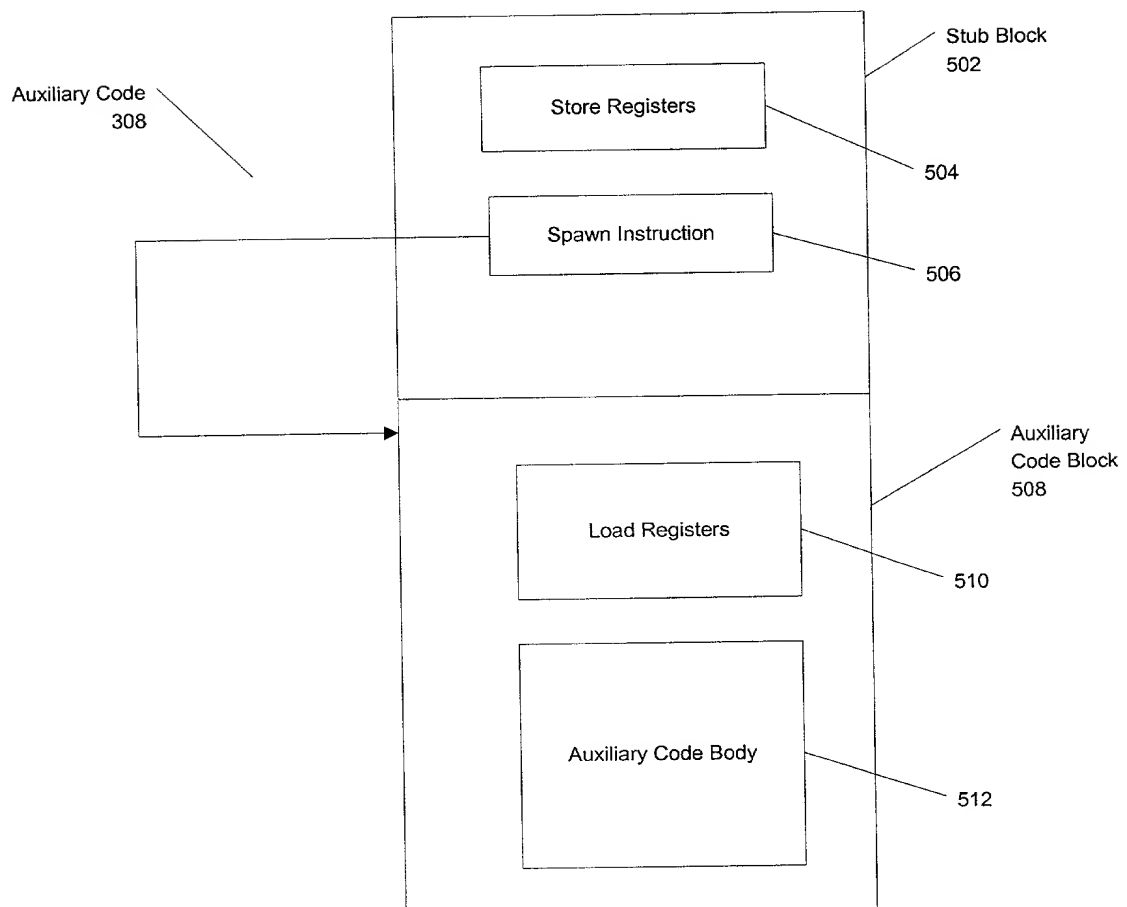


Figure 5

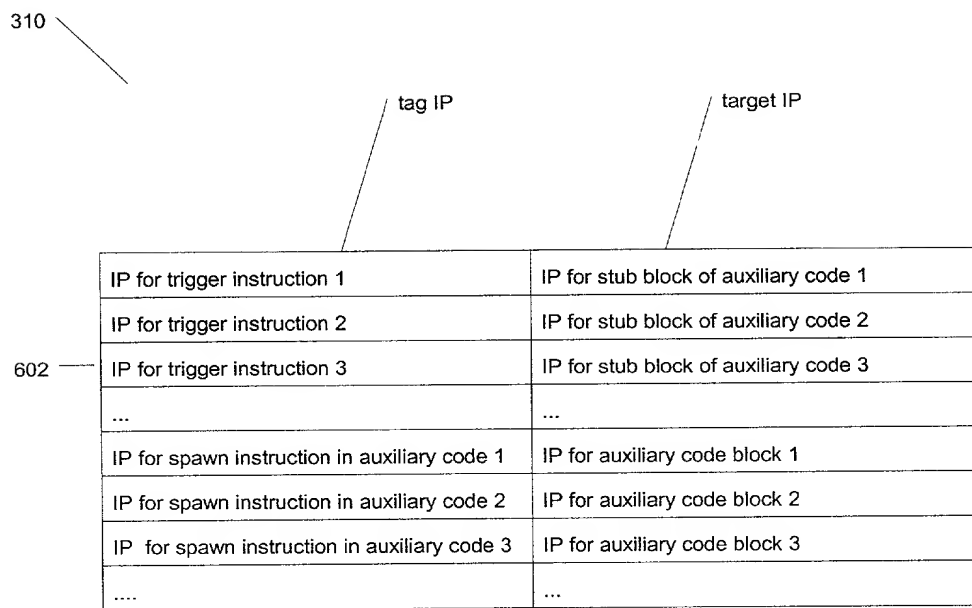


Figure 6

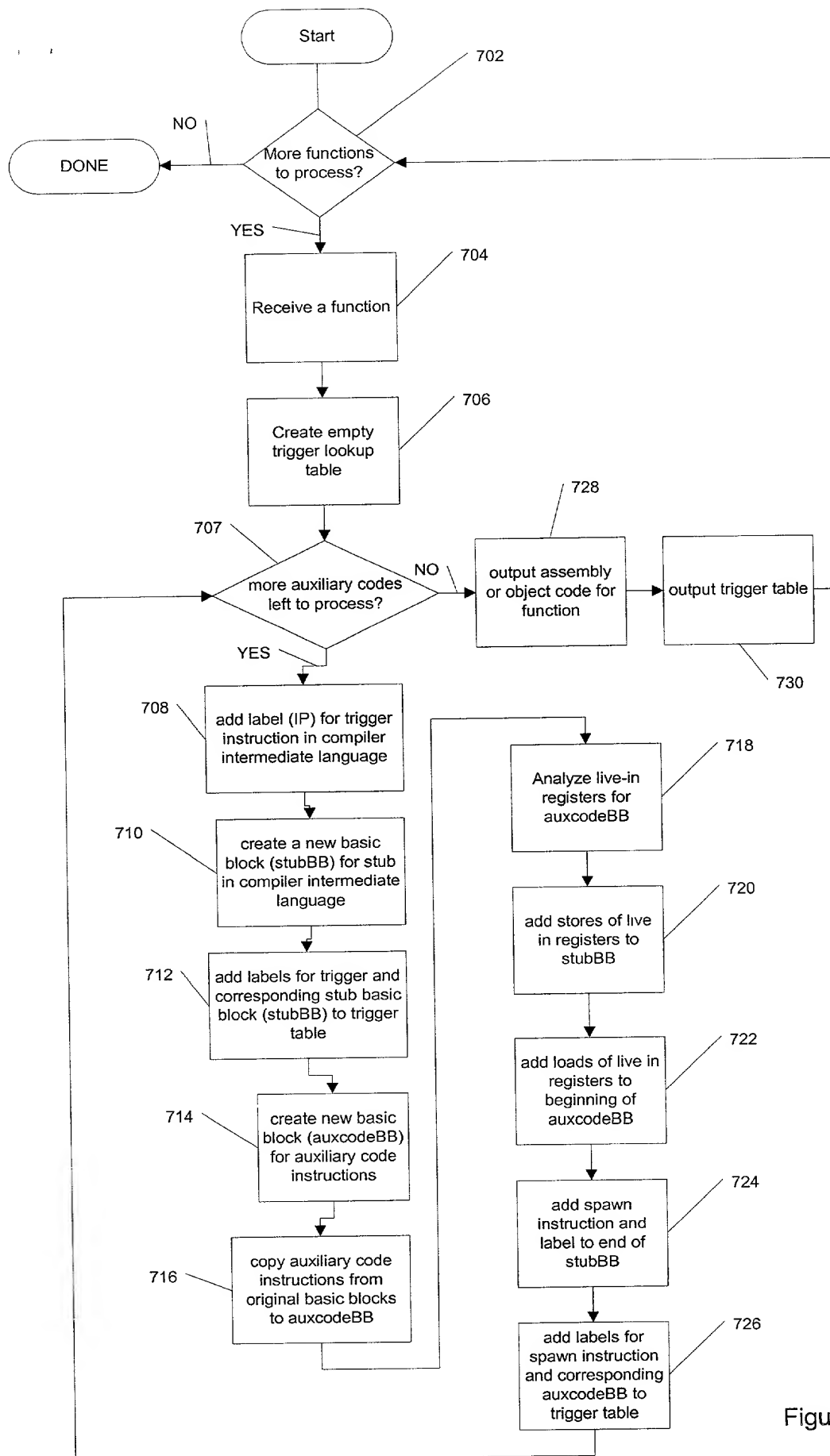


Figure 7